

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	METHODOLOGY FOR VERIFYING MULTI-CYCLE AND CLOCK-DOMAIN-CROSSING LOGIC USING RANDOM FLIP-FLOP DELAYS
First Named Inventor/Applicant Name:	Hin-Kwai Lee
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1		190121US_POA.pdf	121096 3743c715cdcf3e7091c9f619fe107e7314fb1 c5c	yes	2

Multipart Description/PDF files in .zip description			
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